**MTech Semester II Electronics and Communication Engineering - LH7 Langol**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Day | 900-1000 | 1000-1100 | 1100-1200 | 1200-1300 | 1-2 PM | 1400-1500 | 1500-1600 | 1600-1700 |
| M | EC512, VLSI System II Lab, Dr. Ashish Ranjan, Dr. H. Tarun | | |  | L  U  N  C  H | EC504, RTOS, Dr. L. Surajkumar Singh | EC505, ESP, Dr. K. Pritamdas | EC525,LVLSI ,Dr. Ashish Ranjan |
| T | EC512, VLSI System II Lab, Dr. Ashish Ranjan, Dr. H. Tarun | | |  | EC504, RTOS, Dr. L. Surajkumar Singh | EC506, VLSI DSP, Dr Manoj Kumar | EC525,LVLSI, Dr. Ashish Ranjan |
| W | EC512, VLSI System II Lab, Dr. Ashish Ranjan, Dr. H. Tarun | | |  | EC504, RTOS, Dr. L. Surajkumar Singh | EC506, VLSI DSP, Dr Manoj Kumar | Library |
| T | EC512, VLSI System II Lab, Dr. Ashish Ranjan, Dr. H. Tarun | | |  | EC505, ESP, Dr. K. Pritamdas | EC506, VLSI DSP Dr Manoj Kumar | Library |
| F | EC512, VLSI System II Lab, Dr. Ashish Ranjan, Dr. H. Tarun | | |  | EC505, ESP, Dr. K. Pritamdas KPD | EC525, LVLSI , Dr. Ashish Ranjan | Library |